OIPE	EV182663766
	IN THE UNITED STATES PATENT AND TRADEMARK OFFICE 8/I.D.S
T& Application	Serial No. 10/087,416 February 27, 2002 D Jigish D. Trivedi, et all Micron Technology, Inc. Unit 2811
Filing Date	
Inventor	Jigish D. Trivedi, exalt 🔭 🧎
Assignee	Micron Technology, Inc.
Group Art U	Jnit
Examiner	
Attorney's [Docket No

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References - See Attached Form PTO-1449

The Examiner's attention is directed to the references which are listed on the attached Form PTO-1449, copies of which are attached. No admission is made regarding whether all the submitted references are prior art.

Citation of the referenced art is respectfully requested.

Integrated Circuitry (As Amended)

Respectfully submitted,

D. Brent Kenady

Reg. No. 40,045

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